

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **: Yukihiro KIKUCHI, et al.**
Filed: **: Concurrently herewith**
For: **: CONGESTION CONTROL UNIT**
Serial No. **: Concurrently herewith**

October 16, 2001

Assistant Commissioner of Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

S I R:

Prior to the issuance of an Office Action, please amend the specification and claims as follows:

IN THE CLAIMS:

Please amend the claims with the following:

4.(Amended) A congestion control unit according to claim 1, wherein the packet discarding judgment section judges whether an arriving packet is made to pass or is discarded according to the packet discarding probability in which the smooth queue length is used as a parameter.

5.(Amended) A congestion control unit according to claim 1, wherein the smooth queue length calculating section includes a memory device or pointing device for calculating and displaying a quantity of accumulated data composed of a difference between the quantity of input data and the quantity of output data.

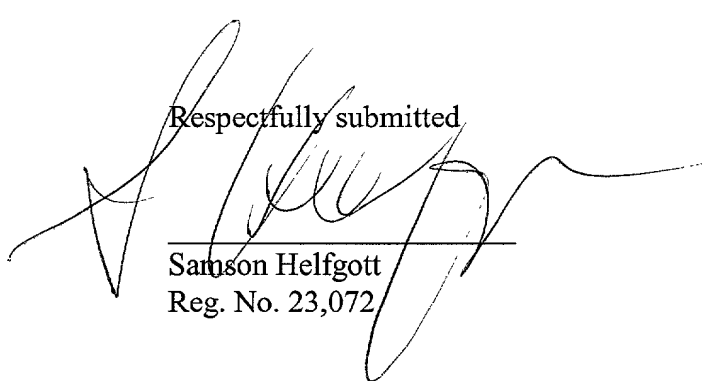
REMARKS

This amendment is being submitted to avoid multiple dependent claims.

Attached hereto is a marked-up version of the changes made to the claims by the current amendment. The attached page is captioned **"Version with markings to show changes made."**

Favorable consideration is respectfully requested.

Respectfully submitted



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Any fee due with this paper, may be charged
On Deposit Acct. No. 50-1290.

VERSION WITH MARKINGS TO SHOW CHANGES MADE

IN THE CLAIMS:

Please amend the claims with the following:

4.(Amended) A congestion control unit according to ~~one of~~ claims 1 ~~to~~ 3, wherein the packet discarding judgment section judges whether an arriving packet is made to pass or is discarded according to the packet discarding probability in which the smooth queue length is used as a parameter.

5.(Amended) A congestion control unit according to ~~one of~~ claims 1 ~~to~~ 4, wherein the smooth queue length calculating section includes a memory device or pointing device for calculating and displaying a quantity of accumulated data composed of a difference between the quantity of input data and the quantity of output data.